## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Filed: Invent	2	***	Examiner: Group/Art Unit: Atty. Dkt. No:	Banankhah, M 2195 MIPS.0177-00-US
Title:	Context Selection And Activation Mechanism For Activating One Of A Group Of Inactive Contexts In A Processor	\$ \$ \$ \$ \$ \$		
	Core For Servicing Interrupts	§ §		

## **AMENDMENT A**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of November 28, 2005, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.

## **IN THE DRAWINGS**

A replacement sheet is provided herein for Figure 50. Figure 50 has been amended to correct a reference numeral. In particular, the reference numeral 5008 which corresponds to the element denoted "L" within RTU 5007, has been replaced with the reference numeral 5009. This change is made to avoid duplication of the reference numeral 5008 with the cache 5008, also shown in Figure 50.